

Fig. 1

110










| 112        |      |              | 114  |   |   |     |   |
|------------|------|--------------|------|---|---|-----|---|
| Lot number | Time | Wafer number | Step | Drawing file  | Drawing file  | ... | Drawing file  |
|            |      |              | 116  |  |  | ... |  |
|            |      |              |      |  |  | ... |  |
| ⋮          | ⋮    | ⋮            | ⋮    | ⋮   | ⋮   | ... | ⋮   |
|            |      |              |      |  |  | ... |  |

Fig. 2

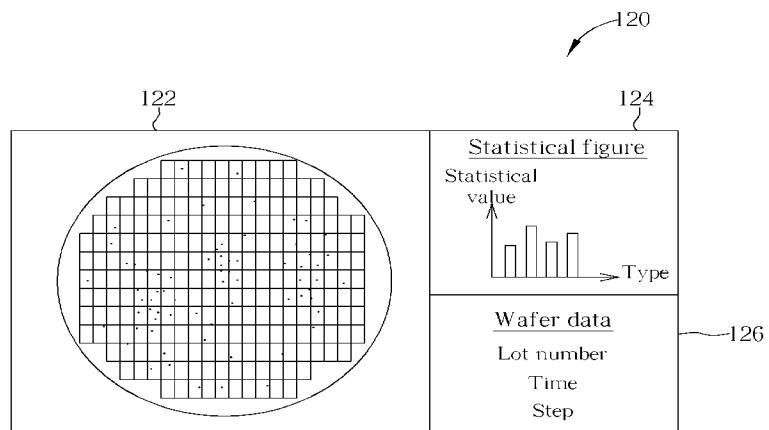


Fig. 3